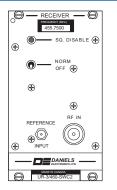


MT-3 Radio Systems

TN261 UR-3/400-S UHF Low Current Receiver



The UR-3/400-S receiver is a low standby current, synthesized FM receiver capable of operating in 12.5 KHz (narrowband) or 25 KHz (wideband) channels. The UR-3/400-S receiver operates in one of two frequency bands: 406 to 430 MHz or 450 to 470 MHz. A modular design allows each of the receiver's three internal modules, 21.4 MHz FM IF/Audio Main Board, FE3 Front End, and OS-3/400 Synthesizer, to be individually assembled and tested. This facilitates construction, tuning and maintenance as well as troubleshooting procedures. The synthesizer module can be programmed to have up to 16 channels exclusive to one frequency band.

Specifications

Frequency Bands 406 - 430 MHz / 450 - 470 MHz

Channel Spacing 12.5 KHz or 25 KHz

Receiver Switching Range $\pm 1 \text{ MHz}$

Reference Sensitivity (12 dB SINAD) $< -116 \text{ dBm } (.350 \text{ }\mu\text{V})$

Adjacent Channel Rejection (Selectivity) > 65 dB (narrowband) / > 70 dB (wideband)

Spurious Response Rejection > 85 dB **Intermodulation Rejection** > 70 dB

Hum & Noise Ratio (20 KHz Low Pass Filter) > 45 dB (narrowband) / > 50 dB (wideband)

L.O. Frequency Stability ± 1.0 ppm (-30 °C to +60 °C) (-40 °C to +60 °C optional) **Modulation Type** ± 1.0 ppm (-30 °C to +60 °C) (-40 °C to +60 °C optional)

Audio Distortion< 2.0% @ 25 °C (< 6.0% @ -40 °C to +60 °C)Receiver Attack Time< 10 ms

Receiver Closing Time < 10 ms

Squelch Threshold / Hysteresis -123 to-105 dBm, adjustable from 2 dB to 20 dB

Audio Output (600 Ω Balanced or Unbalanced)+3.0 dBm De-emphasis/FlatInput Impedance50 Ω (Type N Connector)

Operating Temperature -30 °C to +60 °C (-40 °C to +60 °C optional)

Operating Current (Squelched) < 100 mA

Models Available

UR-3/420-SWC200
UR-3/420-SNC200
UR-3/460-SWC200
UR-3/460-SNC200
UR-3/460-SNC200
UR-3/460-SNC200
UR-3/460-SNC200
Low Current Synthesized, 25 KHz Bandwidth, 406 - 430 MHz
Low Current Synthesized, 25 KHz Bandwidth, 450 - 470 MHz
Low Current Synthesized, 12.5 KHz Bandwidth, 450 - 470 MHz

Receiver Operating Frequency

The receiver is initially aligned at the factory for the frequency stamped on the 'Factory Set Operating Frequency' label on the front panel. For a small frequency change, no re-alignment of the receiver may be required. If the frequency change is greater than ± 1 MHz from the frequency at which the last complete receiver alignment was performed, the **synthesizer** and **front end** will need to be realigned. To align and / or adjust the receiver the outer cover needs to be removed, the receiver needs to be plugged into the subrack via a cable and / or extender card and power must be applied to the system.

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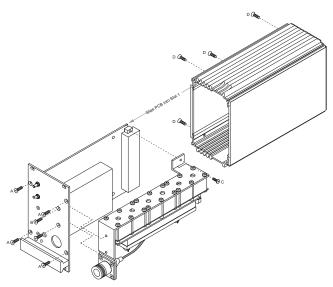
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Receiver Alignment Procedures



Remove the four front panel screws (A) and four side panel screws (D) to slide the receiver outer cover off and expose the IF / Audio Main Board Local Oscillator and Front End Assemblies. Remove the two front panel screws (B) and internal screw (C) to remove the Front End for easier access to the Local Oscillator.

Front End Alignment:

Alignment for the Low Current Front End consists of tuning the five section preselector filter only. There are two methods of tuning the Low Current Front End. The preferred method of tuning the Low Current Front End is to use a Spectrum Analyzer with a Tracking Generator. Ensure that the +9.5 Vdc supply is connected to the Front End (red wire). Connect the Tracking Generator output at a level of -20 dBm to the Front End's RF input. Connect the Spectrum Analyzer input to the Front End's IF output. Adjust the helical filter trimmer capacitors for a flat response centered at the desired RF frequency. The alternate method of tuning the Low Current Front End is to monitor receiver SINAD. Inject the desired RF signal to the RF input connector at a level of -116 dBm and adjust the helical filter trimmer capacitors for best receiver SINAD (>-116 dBm).

Synthesizer Alignment:

Using a high impedance (10 M Ω) DC Voltmeter, measure the PLL control voltage at TP4 located on the synthesizer module main circuit board. Using a small standard blade screwdriver, carefully adjust the VCO frequency "tune" trimmer capacitor C5 until a test point voltage of +3.5 Vdc is obtained. Access to TP4 and C5 is available through the synthesizer top cover.

Squelch Adjustments:

Receiver squelch action is factory set to establish a squelch hysteresis window of 6 dB centred about the point of receiver 12 dB SINAD sensitivity. eg. If the receiver sensitivity point is -116 dBm the receiver should be set to unsquelch at -113 dBm and squelch at -119 dBm. Adjustment to the squelch circuitry should be the last receiver alignment step performed. Rotate the squelch hysteresis adjust potentiometer (R115) fully counter clockwise. Rotate the squelch threshold potentiometer (R88) fully clockwise. Inject a standard signal at the desired unsquelch level. Slowly adjust the squelch threshold potentiometer (R88) counter clockwise until the receiver unsquelches. Advance R115 (hysteresis) clockwise until sufficient hysteresis prevents any oscillating COR action at the squelch threshold point. Cycle the RF source off and on while adjusting R88 (threshold) until squelch triggering occurs at the desired signal level. Adjust R115 (hysteresis) clockwise to increase the squelch hysteresis window. Slowly lower the RF source signal level and monitor the point at which the receiver squelches. Increase or decrease R115 (hysteresis) to achieve the desired hysteresis window.

Note: For complete alignment procedures, refer to the instruction manual. These notes are for reference only.

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